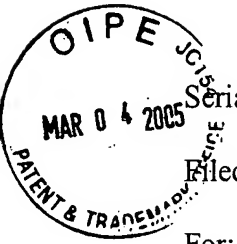


DFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Applicant: Jack Hwang et al.  
Serial No.: 10/644,417  
Filed: August 20, 2003  
For: Forming Strained Source Drain  
Junction Field Effect Transistors

§  
§ Art Unit: 2822  
§  
§ Examiner: Mark V. Prenty  
§  
§ Docket: ITL.0764D1US  
§ P14416D  
§  
§ Assignee: Intel Corporation  
§

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO PAPER NO. 20050125**

Sir:

In response to the office action mailed January 28, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: March 1, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Cynthia L. Hayden*  
Cynthia L. Hayden